

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	66	host near adapter near board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/12 10:49
L3	7	host near adapter near board and clock and pci and timing	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/12 12:30
L4	4	hab and clock and pci and timing	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/12 10:50
L5	2	host near adapter same test\$3 same pci and (stor\$3 memory) with address\$4 with pci and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/12 11:54
L6	3	host near adapter same test\$3 same pci and (stor\$3 memory) with address\$4 same pci and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/12 11:55
L7	3	702/117.ccls. and host near adapter	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/12 12:30
L8	1	702/120.ccls. and host near adapter	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/12 12:30
L9	1	702/124.ccls. and host near adapter	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/12 12:30
L10	0	702/125.ccls. and host near adapter	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/12 12:30
L11	2	702/117.ccls. and host near adapter and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/12 12:31
L12	1	702/120.ccls. and host near adapter and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/12 12:31
L13	0	702/124.ccls. and host near adapter and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/12 12:32

L14	2	714/55.ccls. and host near adapter and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/12 12:33
L15	1	714/56.ccls. and host near adapter and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/12 12:32
L16	14	713/600.ccls. and host near adapter and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/12 12:33
L17	1	713/600.ccls. and host near adapter near board and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/12 12:33
S1	0	test with mode with adapter near board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/09 11:40
S2	2	test with mode same adapter near board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/27 12:15
S3	1	test with adapter near board and PCI with signal	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/27 12:16
S4	1	test same adapter near board and PCI with signal and timing	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/27 12:17
S5	325	timing with PCI with signal	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/27 12:17
S6	23	timing with PCI with signal and adapter with board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/27 12:18
S7	1	timing with PCI with signal and adapter with board and pci.ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/27 12:18
S8	78	test with adapter near board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/26 11:49

S9	11	test with adapter near board and clock near signal	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/26 11:49
S10	2	test with adapter near board and clock near signal and pci	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/26 11:51
S11	0	tim\$4 with pci and test with adapter near board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/26 11:52
S12	22	clock with signal with adapter with board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/26 11:52
S13	9074	clock with signal with adapter with board and pci with adapter with board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/26 11:52
S14	5	clock with signal with adapter with board and pci with adapter with board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/26 11:53
S15	1	clock with signal with adapter with board and pci with signal with generat\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/26 11:54
S16	329	timing with pci with signal	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/26 11:54
S17	134	timing with pci with signal and test\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/26 11:54
S18	1	timing with pci with signal and test\$4 with adapter with board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/26 11:55
S19	1	timing with pci with signal and test\$4 with adapter with board and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/26 11:57
S20	1	timing with pci with signal and test\$4 with adapter with board and clock and pci	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/26 15:42

S21	0	timing with interconnect with signal and test\$4 with adapter with board and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/26 15:43
S22	10	timing with signal and test\$4 with adapter with board and clock and interconnect	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/26 15:43
S23	1	timing with signal with line and test\$4 with adapter with board and clock and interconnect	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/26 15:45
S24	3	timing with signal with line and test\$4 with adapter with host and clock and interconnect	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/26 15:45
S25	4	timing with signal with line and test\$4 with adapter with host and clock and pci	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/26 15:55
S26	757	auve.xp.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/26 15:55
S27	81	test with adapter near board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/09 11:40
S28	4	test with adapter near board and clock and pci	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/09 11:40
S29	18	timing with pci with signal with line	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/09 11:41
S30	8	timing with pci with signal with line and test\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/09 11:43
S31	7	timing with pci with signal with line and adapter	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/12 10:49